

A block diagram of a multi-stage processing system 10. The system is divided into three main sections by vertical dashed lines. The first section contains a block 18 with an internal block 34, receiving inputs 30 and 32. A dashed box 38 is shown above it. The output of block 18 is signal 20, which enters block 22. Block 22 has a dashed box 46 above it and an output 26. The output of block 22 is signal 24, which enters block 48. Block 48 contains a dashed box 54 and a block 52. A dashed box 60 is below block 48, and a dashed box 62 with sub-element 64 is below block 52. The final output of the system is signal 14, labeled as 50.

Figure 1 is a block diagram of a system architecture. The system includes three main processing blocks: 10, 70, and 78. Block 10 receives an input 16 and produces an output 14. Block 70 receives input 14 and produces an output 72. Block 78 receives input 72. There are also feedback paths: 76 from block 70 to block 10, and 80 from block 78 to block 10. A dashed line 74 connects block 70 to block 78.